

Fig. 1A

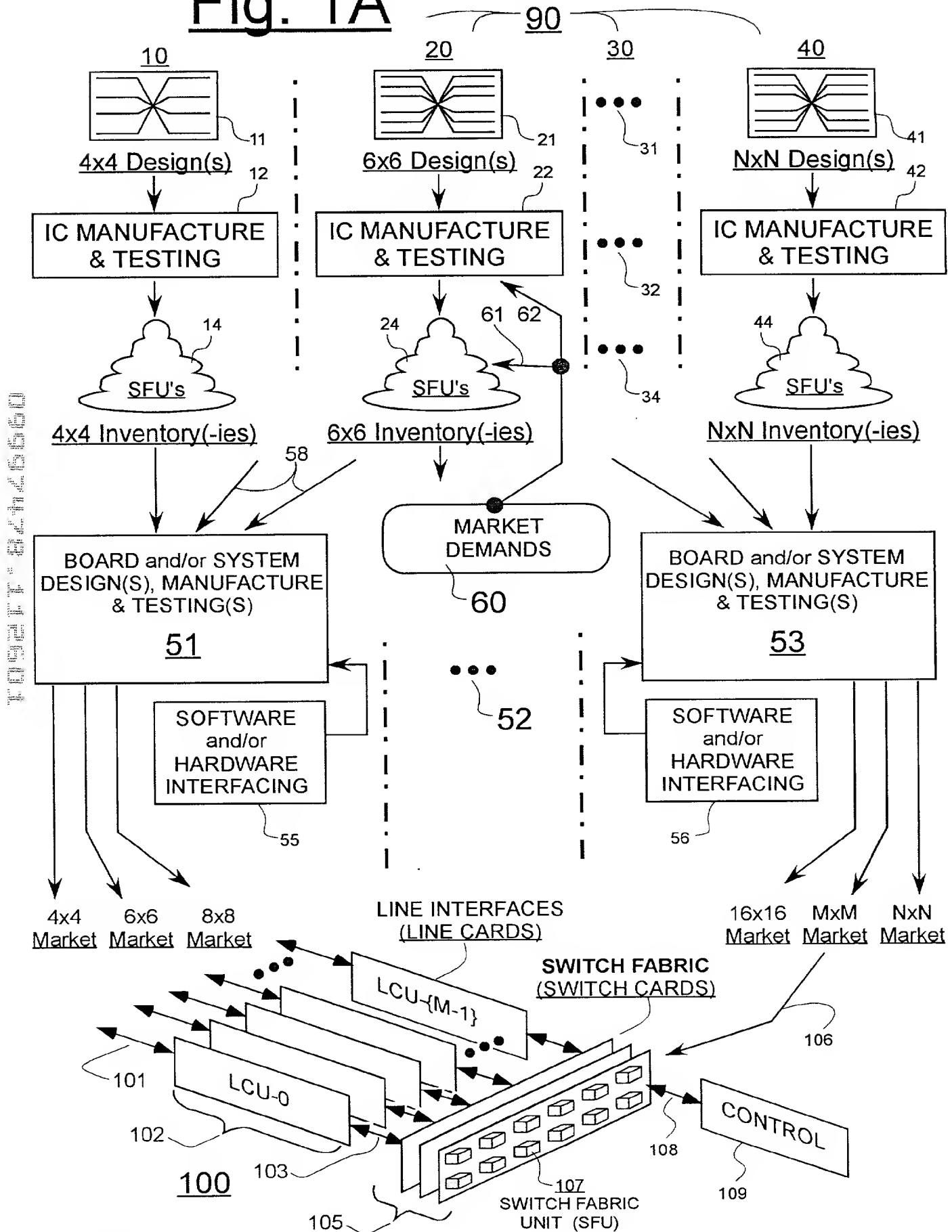


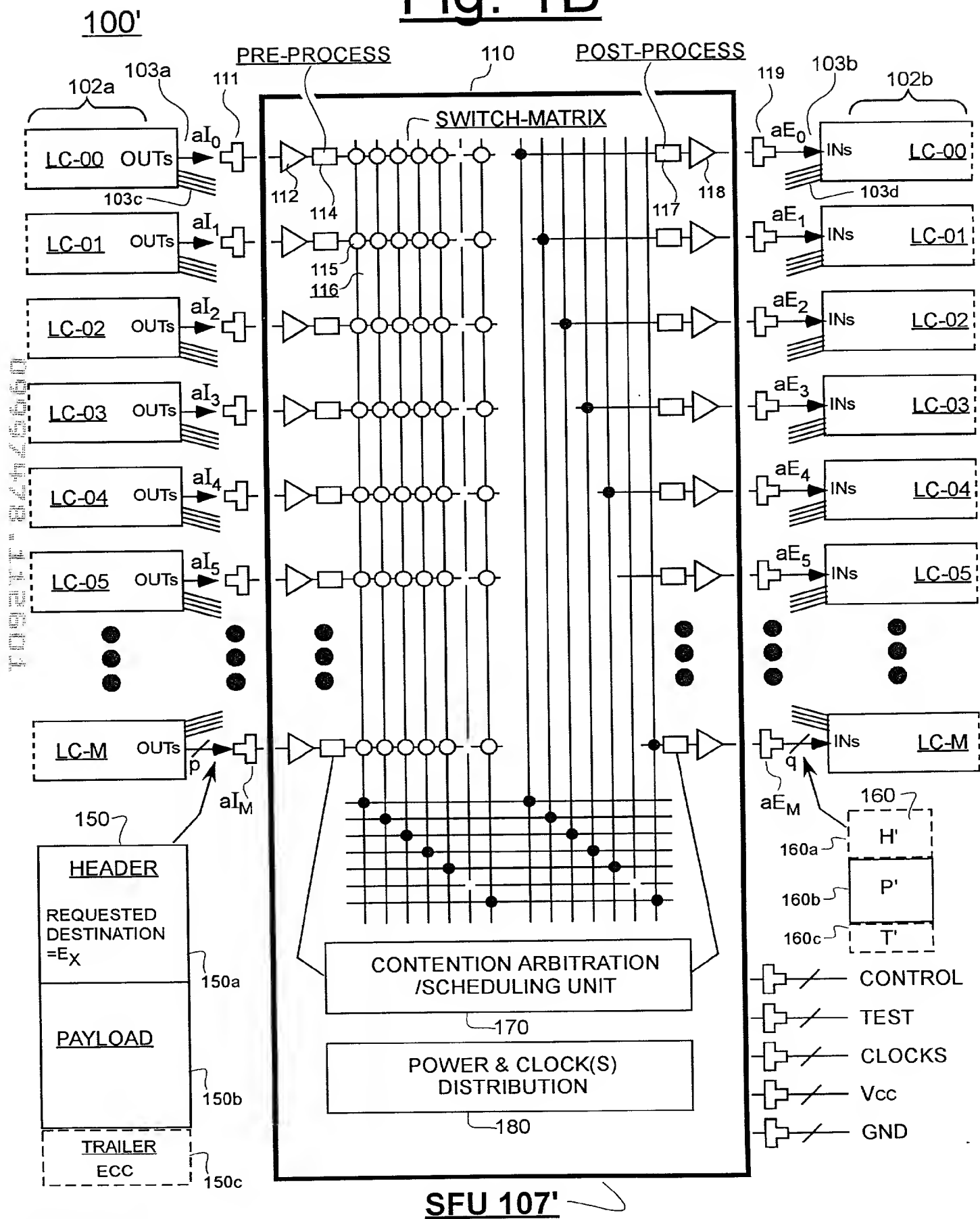
Fig. 1B

Fig. 2A
200

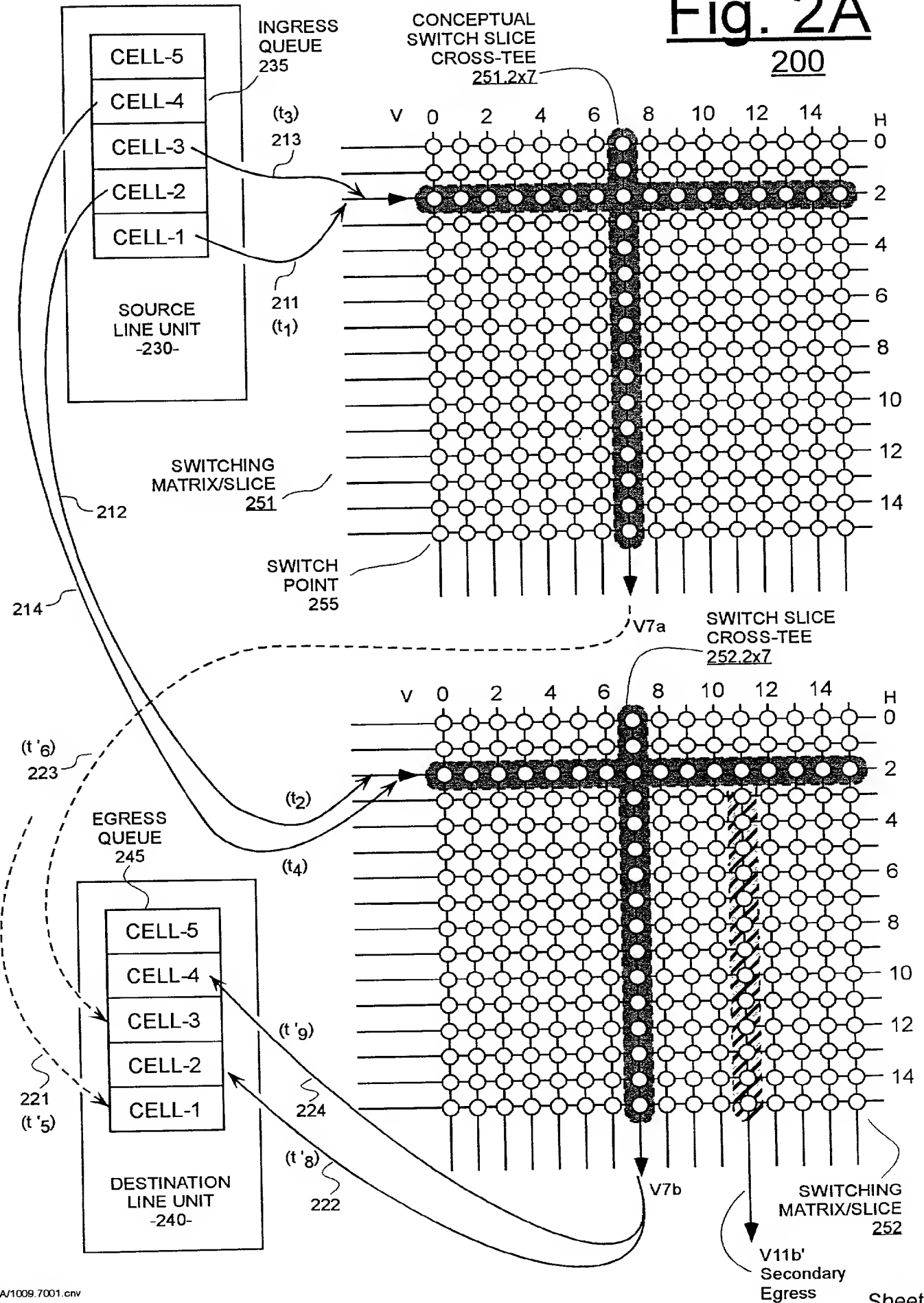
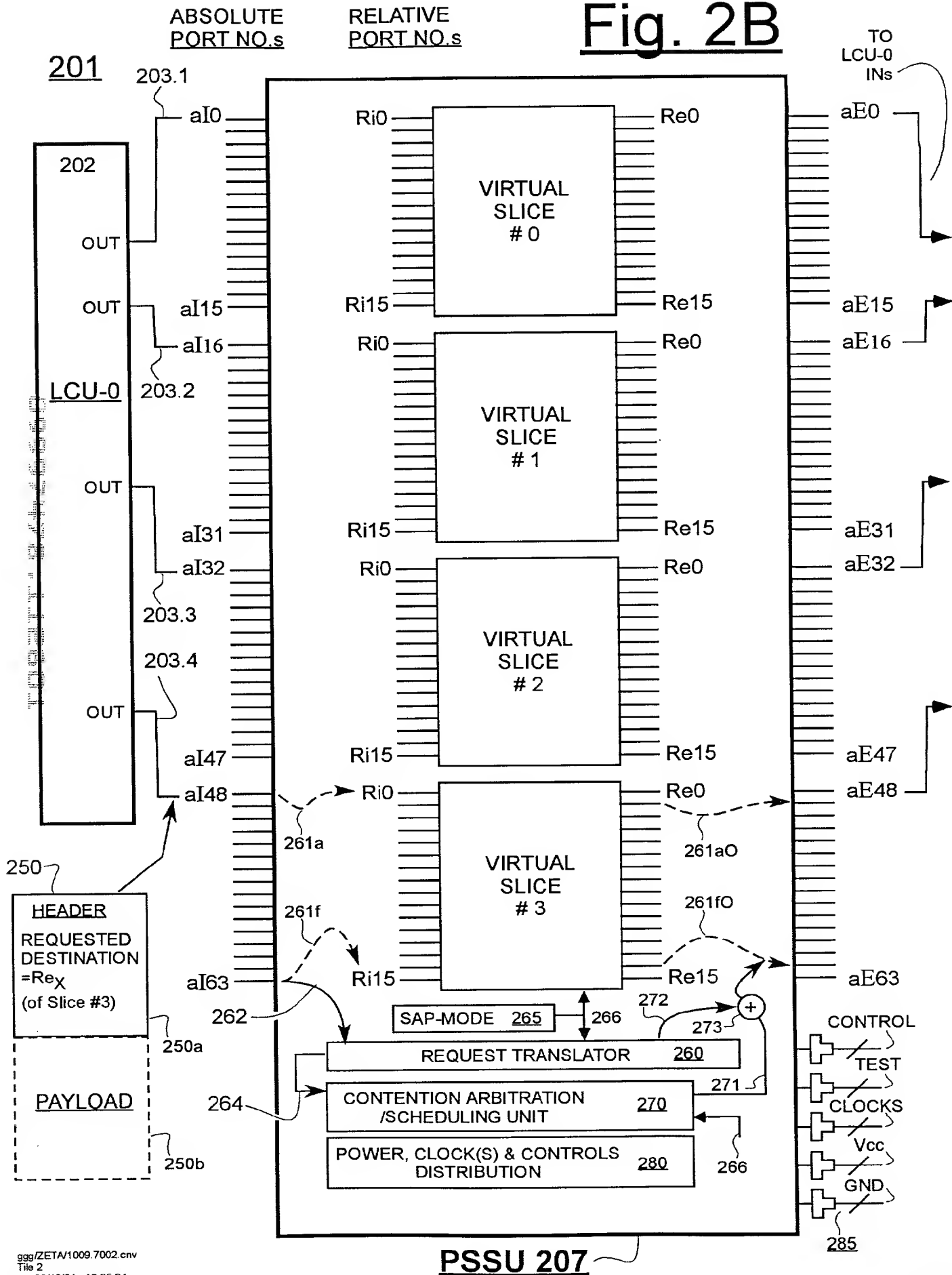
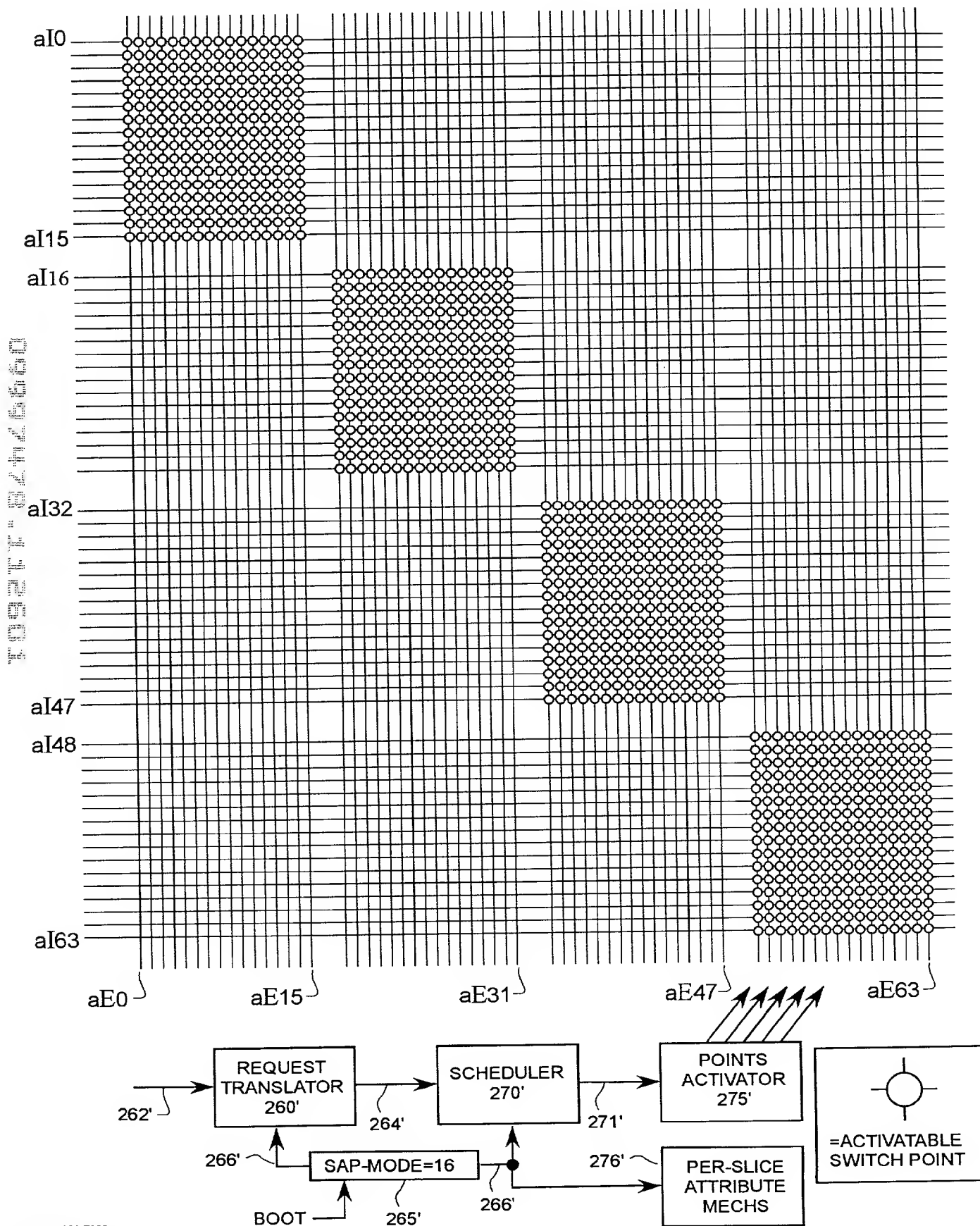
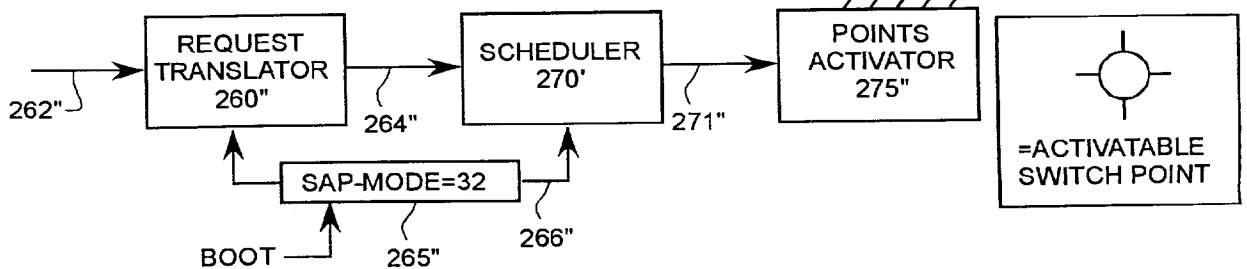
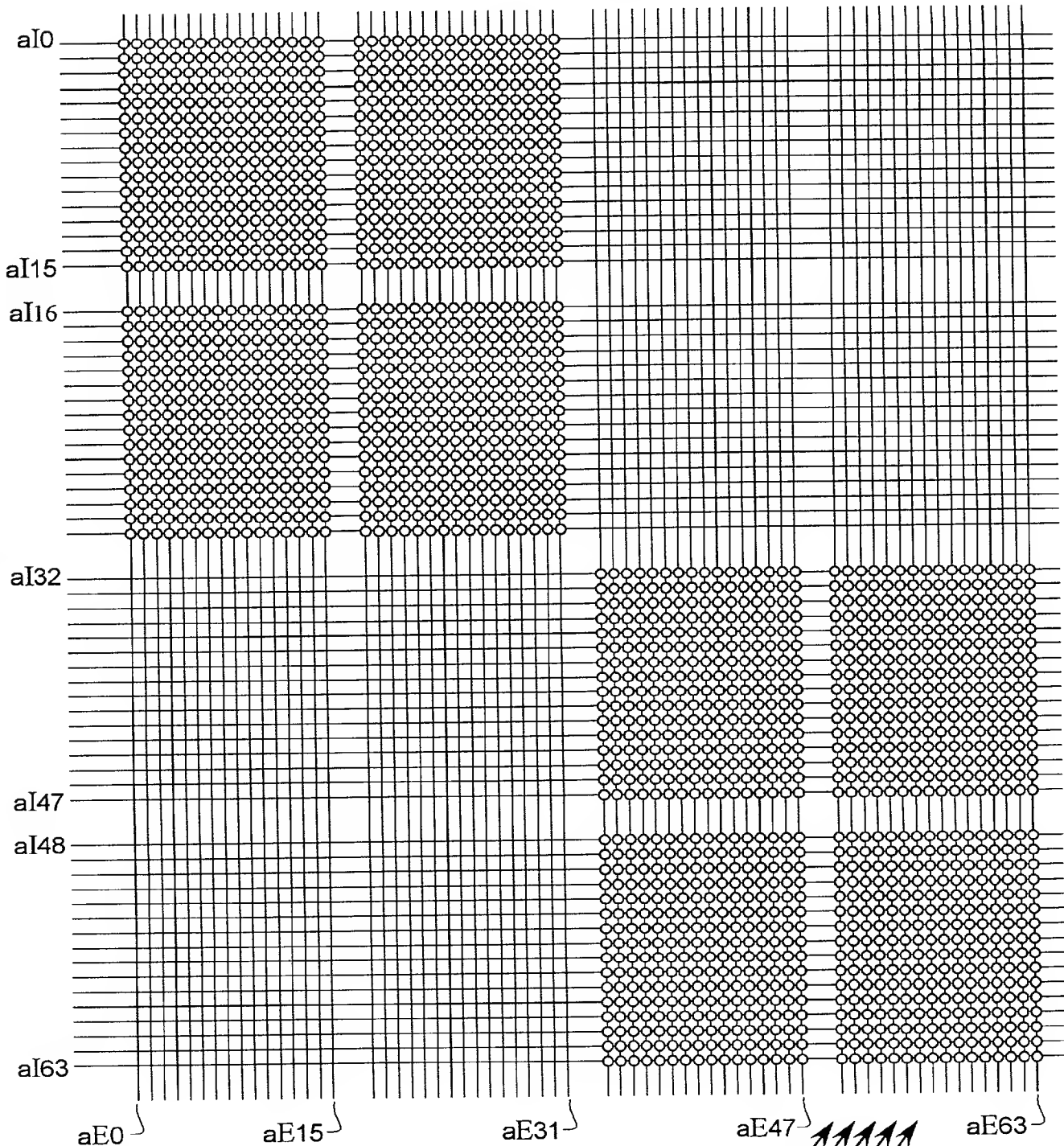


Fig. 2B

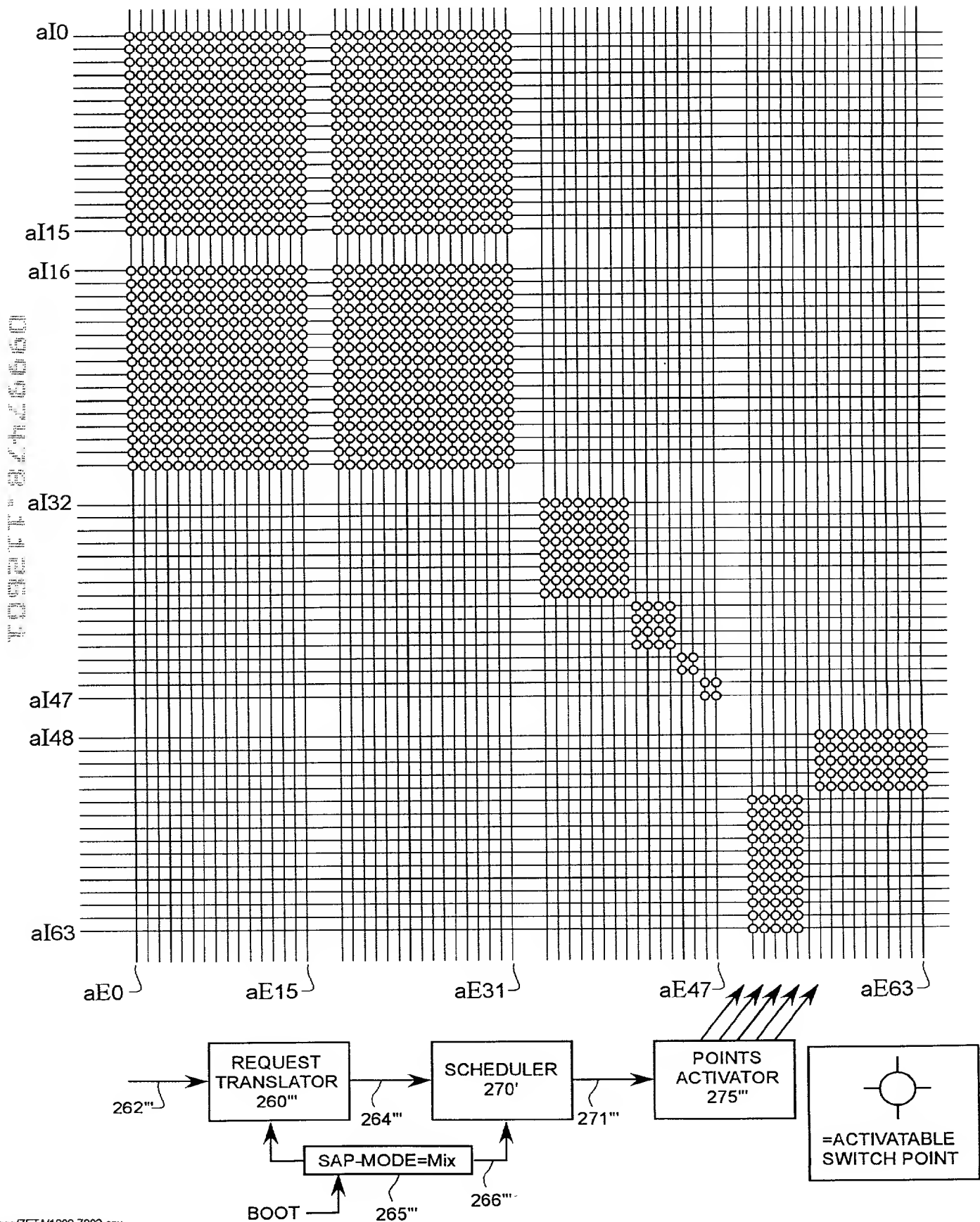
207'

Fig. 2CSAP-16 MODE (25% SLICES)

207" **Fig. 2D** SAP-32 MODE (50% SLICES)



207''' Fig. 2E MIXED SLICES



307



Fig. 3B

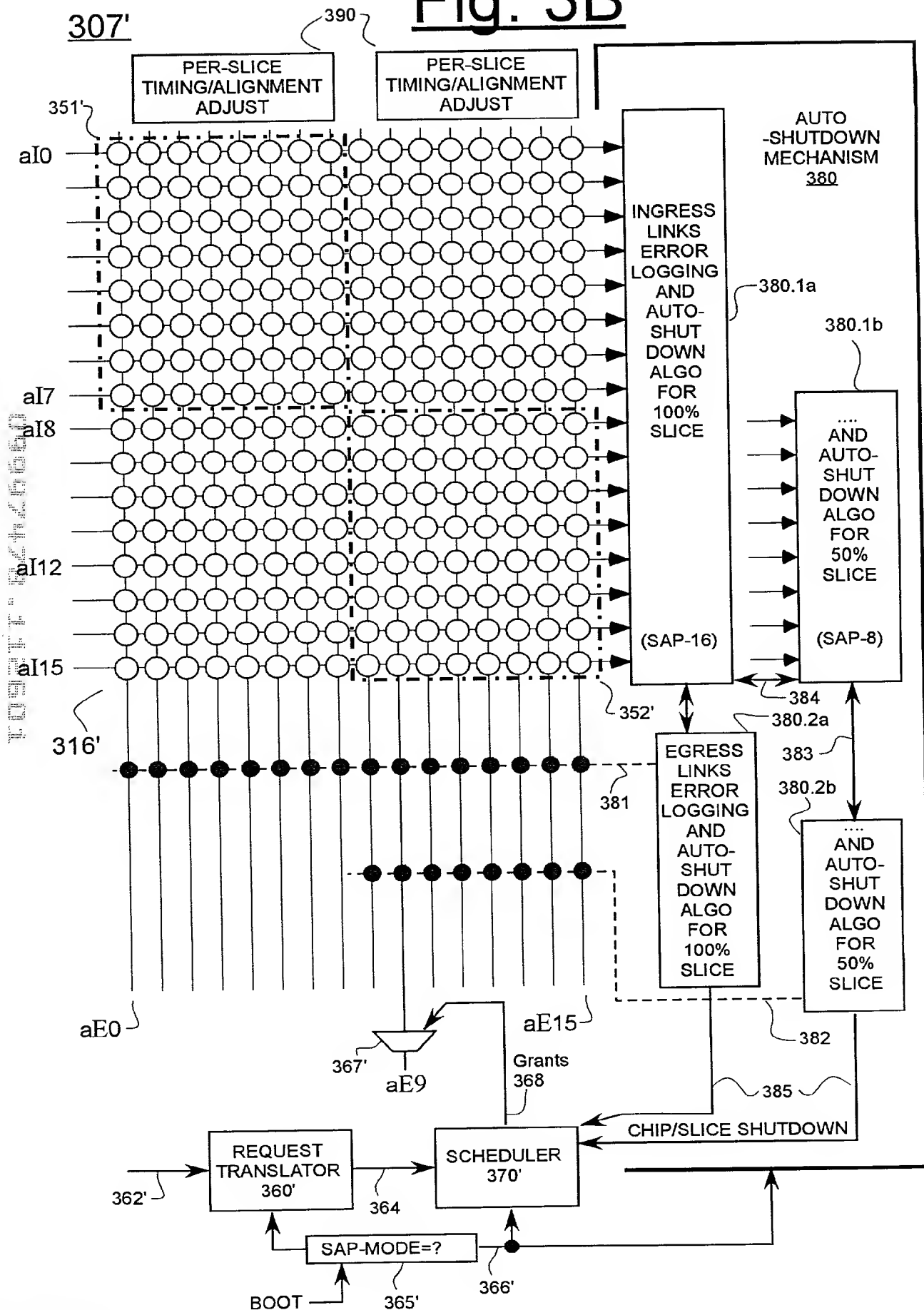


Fig. 3C

300

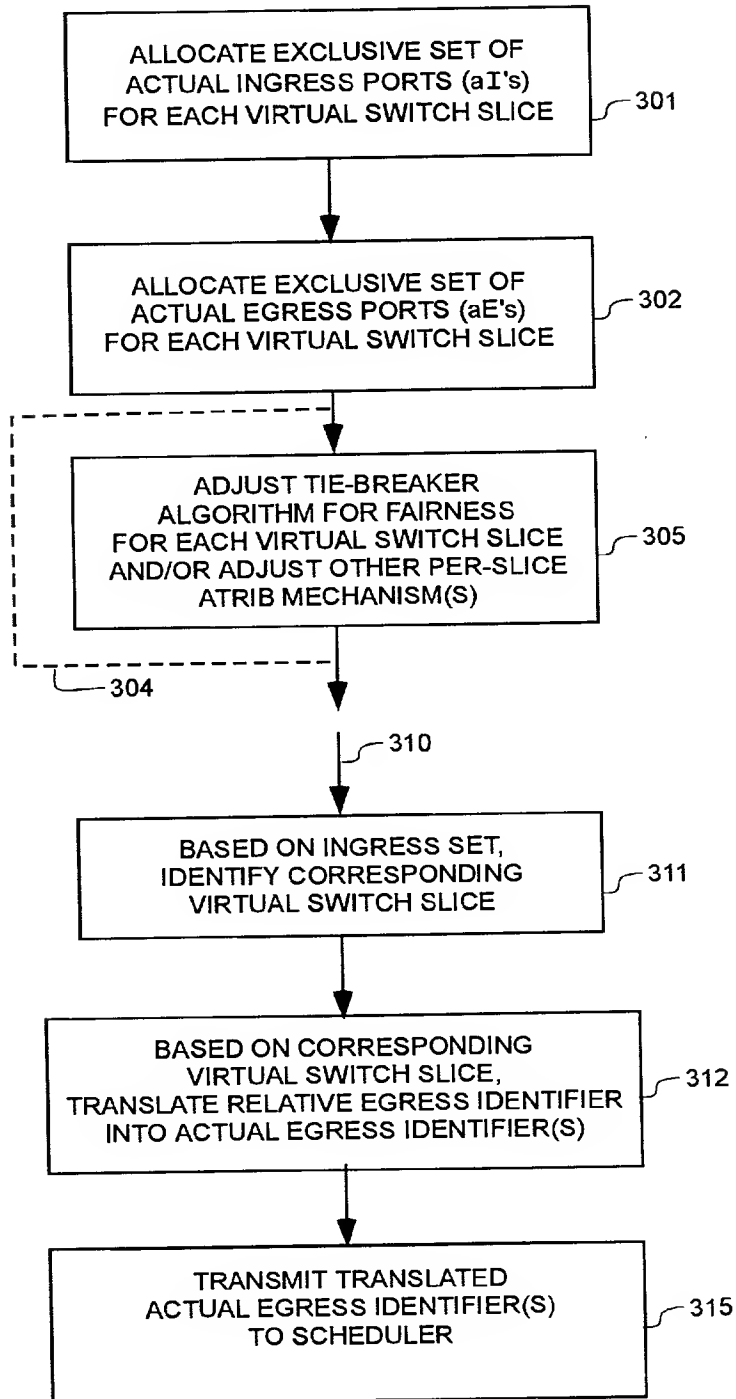


Fig. 4

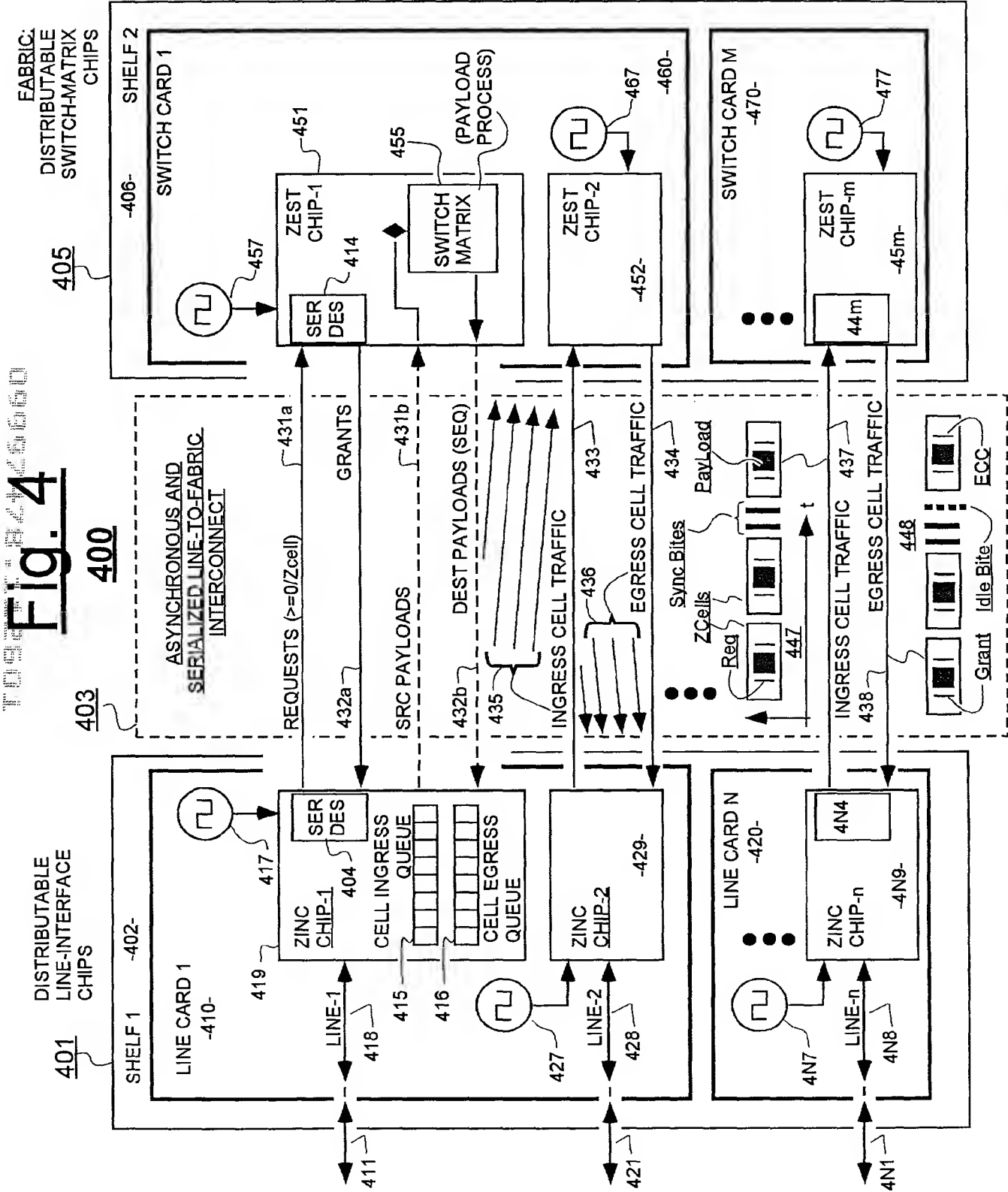


Fig. 5

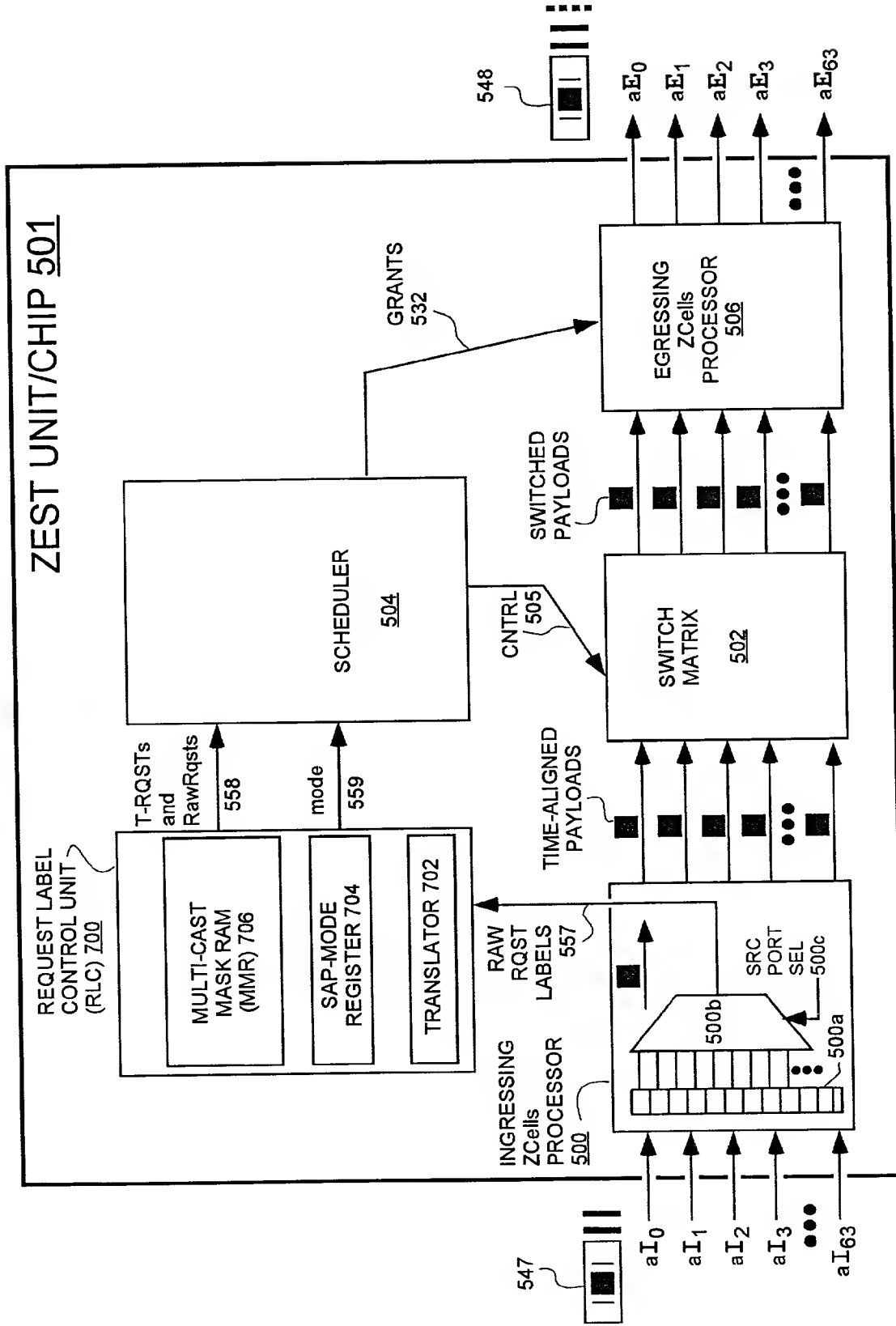


Fig. 6B

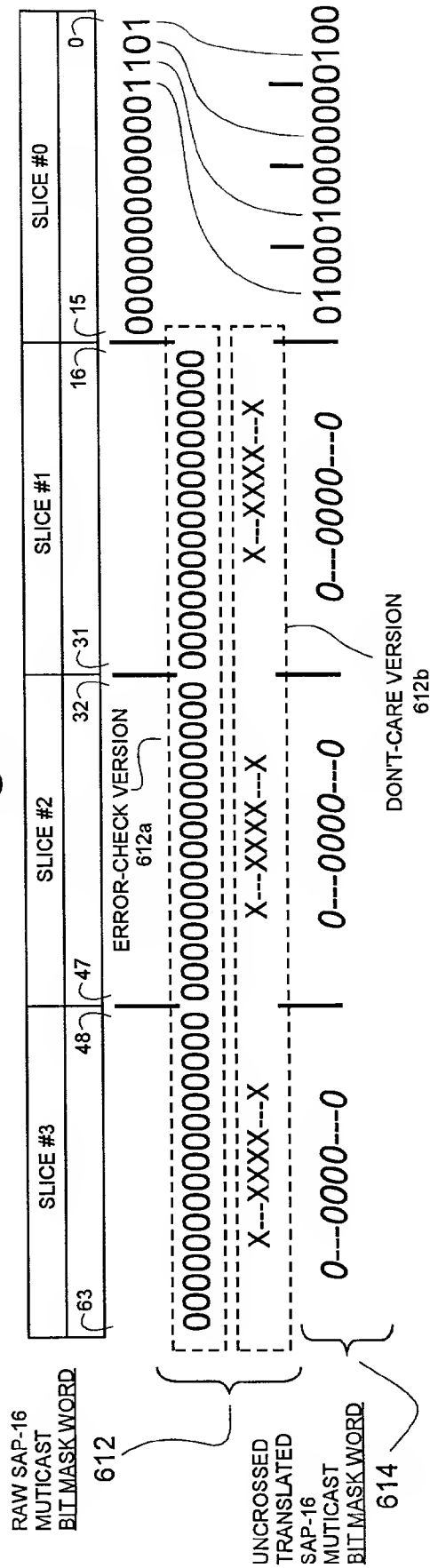


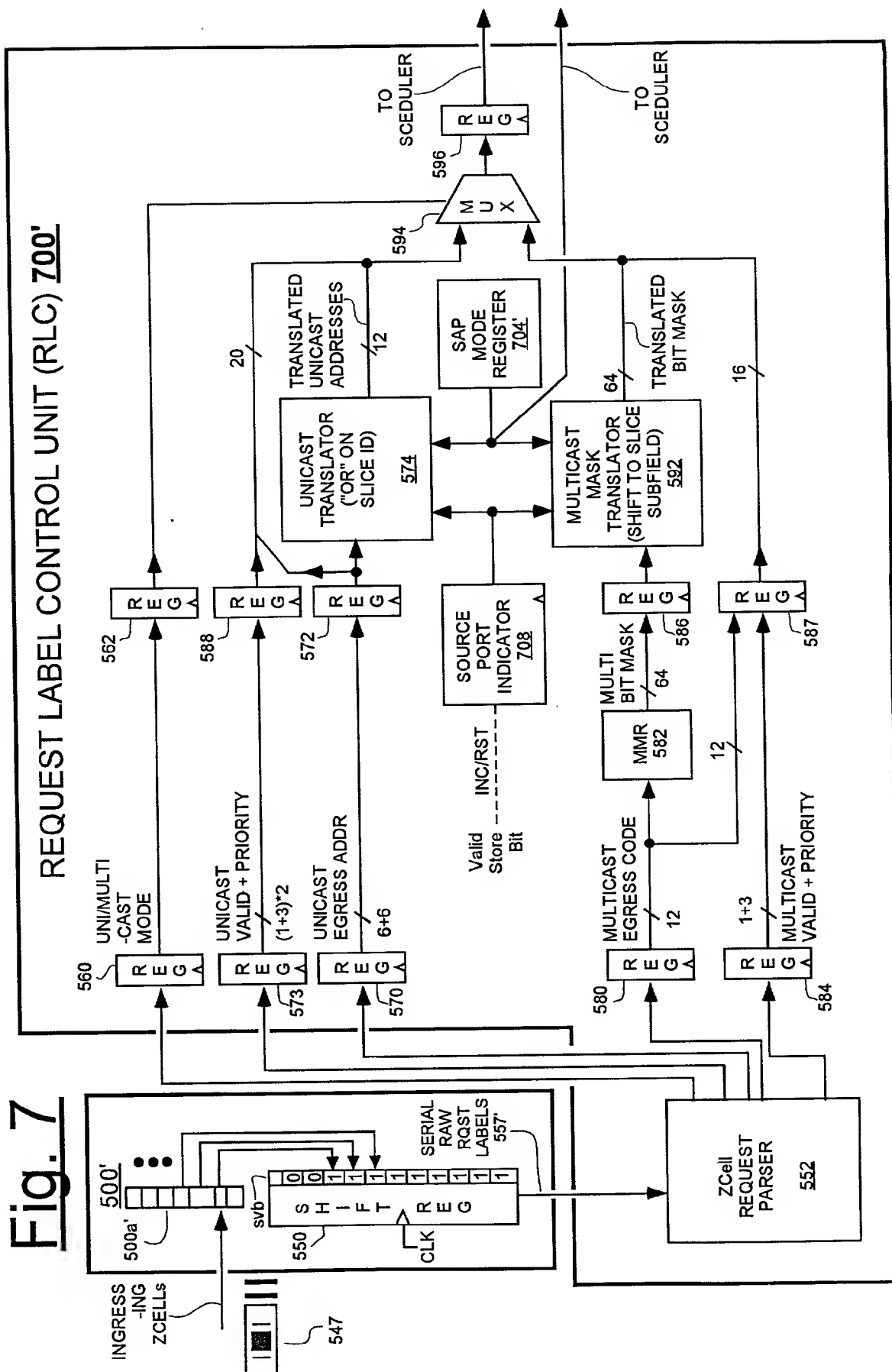
Fig. 7

Fig. 8A

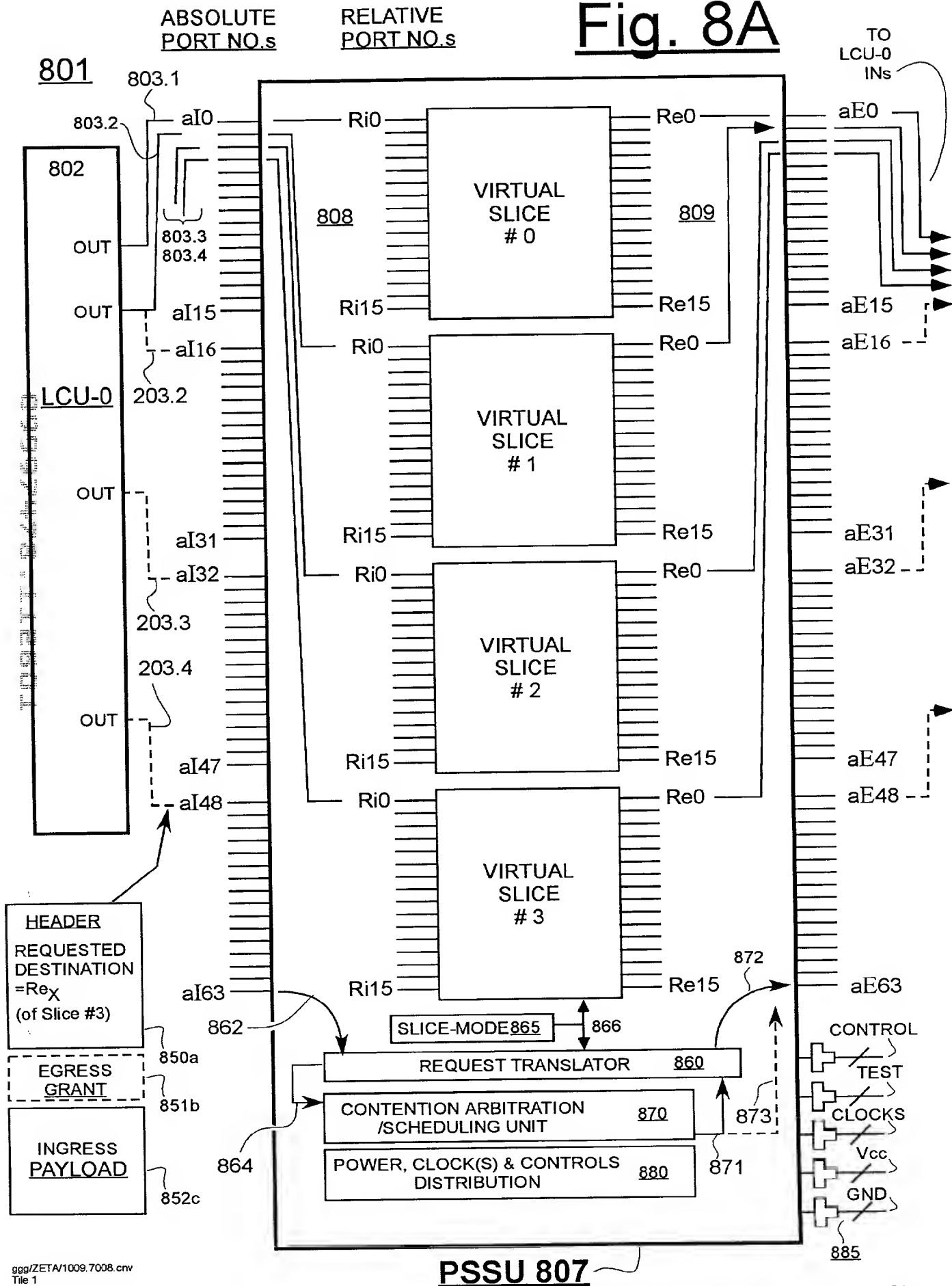


Fig. 8B SAP-16 MODE (UNCROSSED INPUTS)

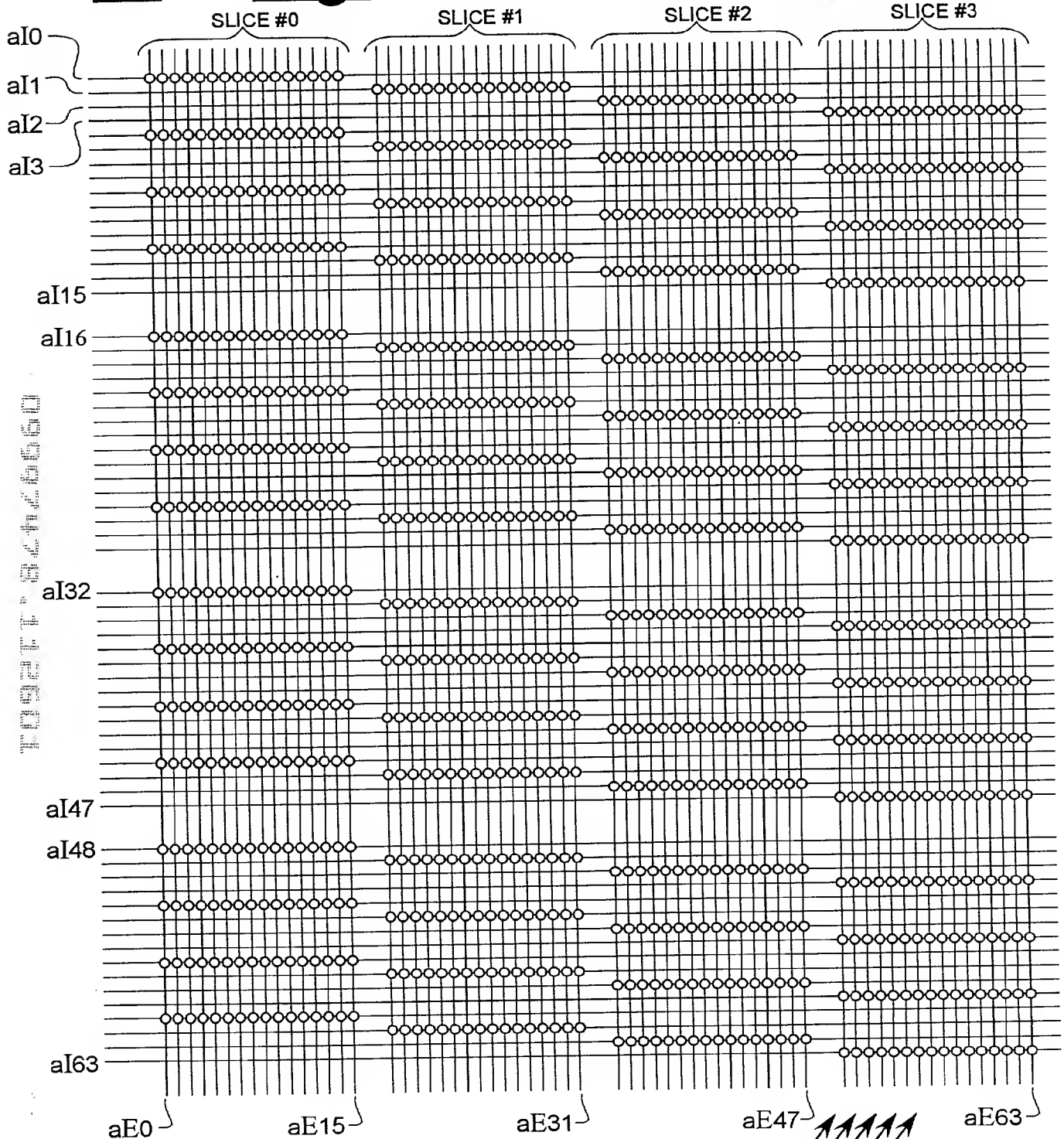


Fig. 8C SAP-16 MODE (UNCROSSED
OUTPUTS & INPUTS)

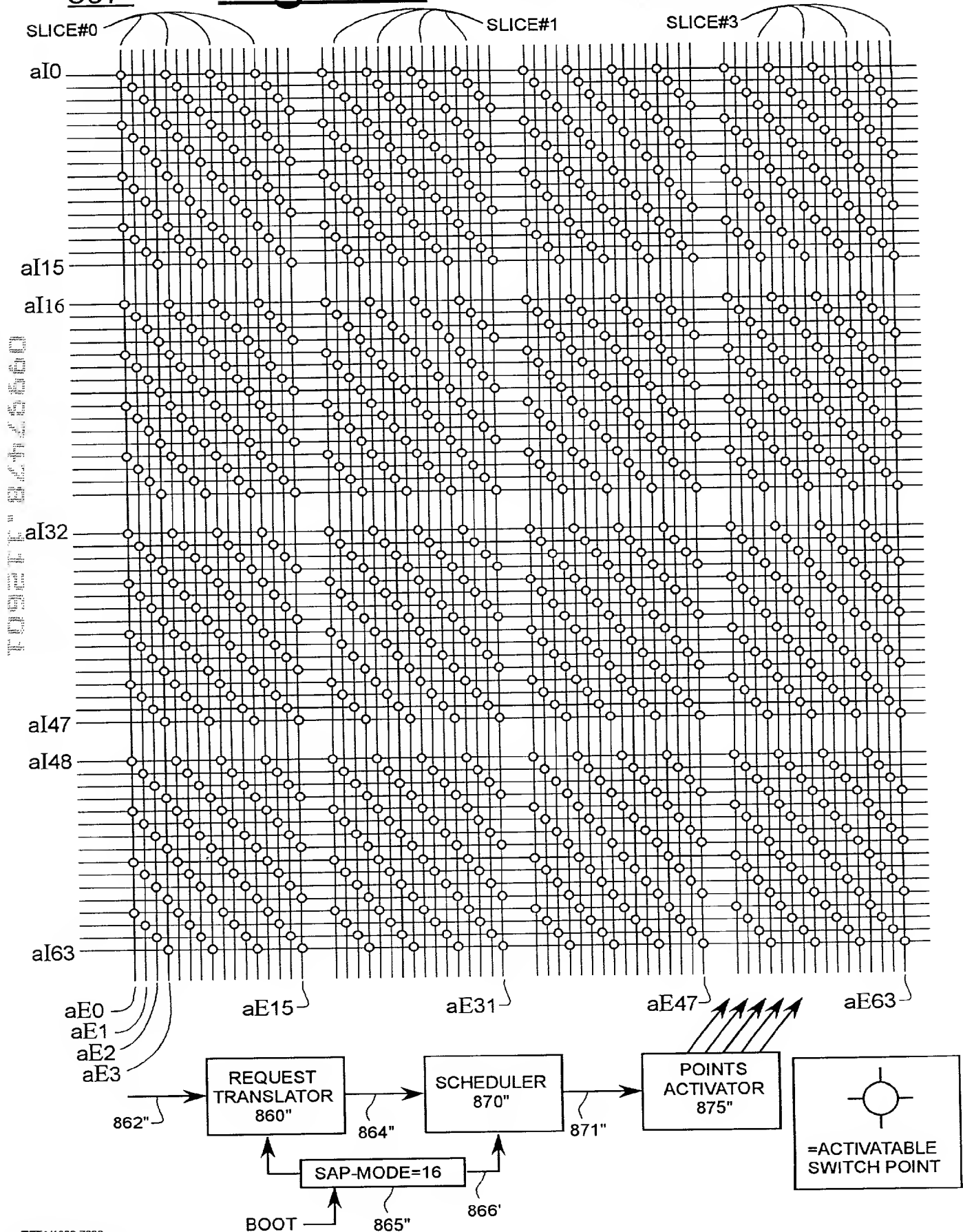


Fig. 8D

**UNCROSSED
UNICAST**

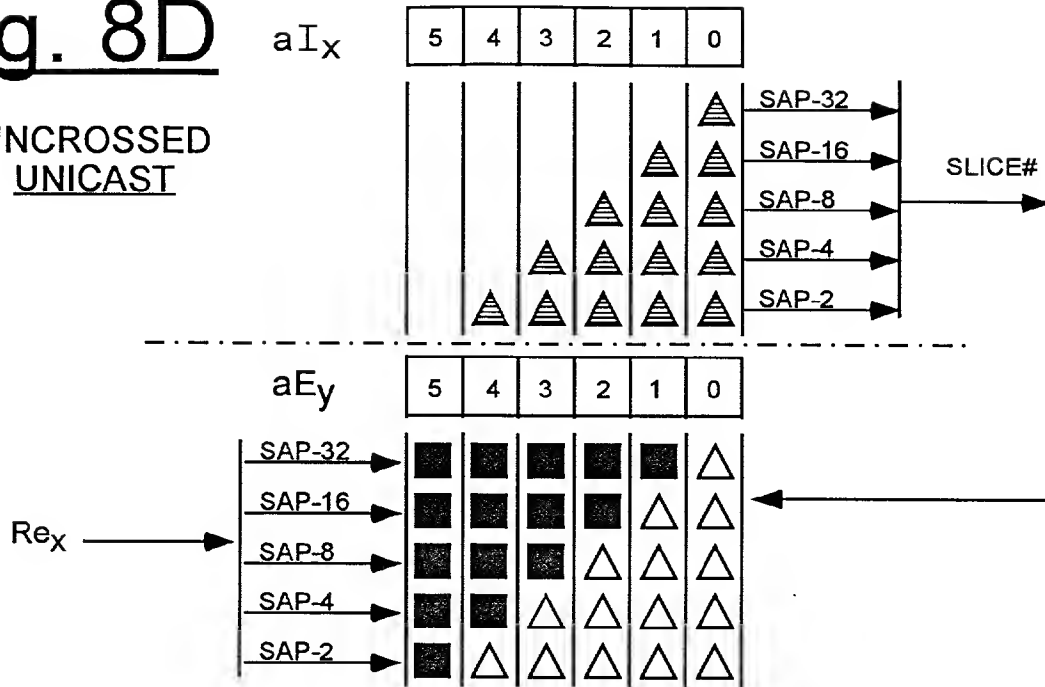


Fig. 8E

**UNCROSSED
MULTICAST**

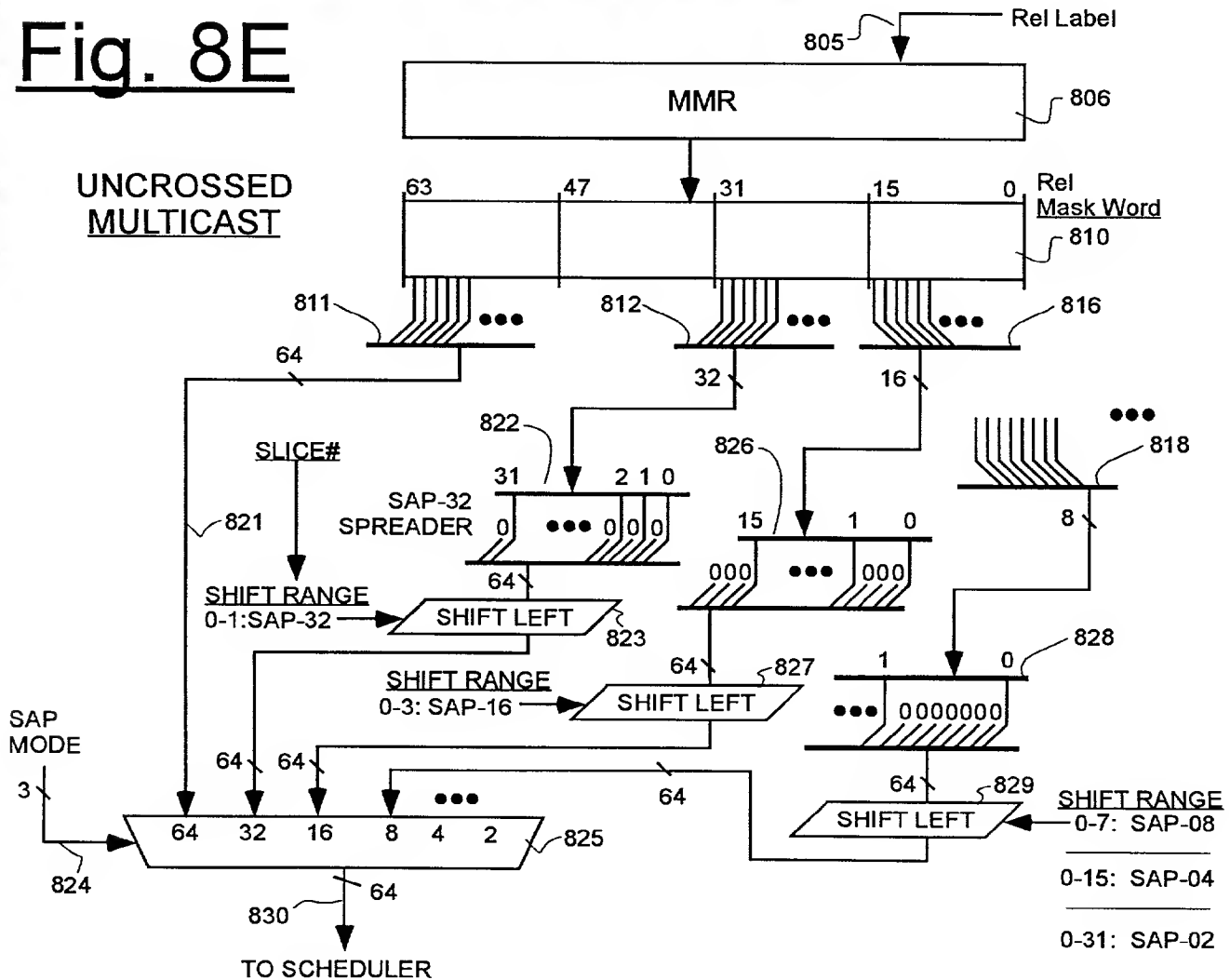


Fig. 8F

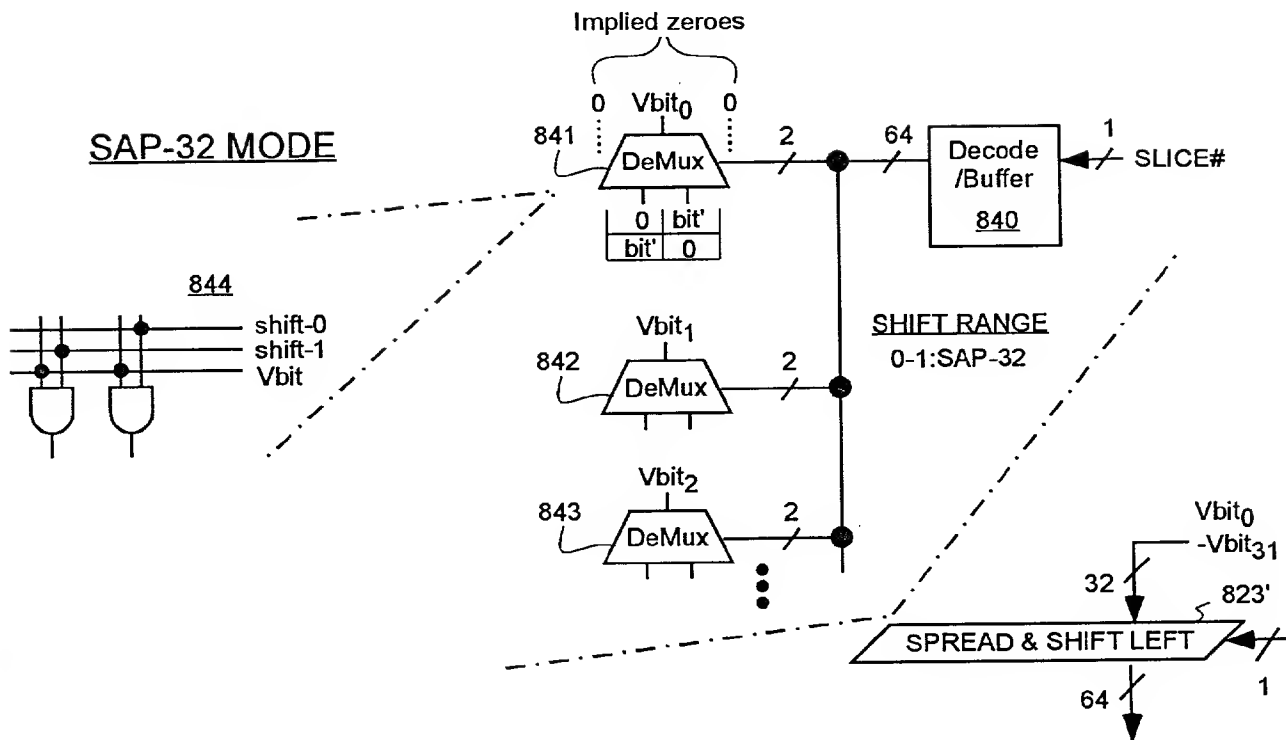


Fig. 8G

